

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S68	1	("5721923").URPN.	USPAT	OR	ON	2004/12/21 15:00
S69	14	("4319090"   "4497978"   "4912698"   "4959854"   "4974256"   "5161154"   "5189667"   "5263083"   "5303369"   "5325525"   "5329579"   "5408334"   "5473773"   "5628013").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/21 15:01
S70	4	("5263083").URPN.	USPAT	OR	ON	2004/12/21 15:42
S71	5	("3755625"   "4449238"   "4771417"   "4912758"   "4949335").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/21 15:44
S73	1	"4912758".pn.	US-PGPUB; USPAT	OR	ON	2005/04/07 13:38
S72	1	"4912798".pn.	US-PGPUB; USPAT	OR	ON	2005/04/07 13:38
S62	85	(370/286 379/406:\$2).ccls. and ((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3)) with (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)	US-PGPUB; USPAT	OR	ON	2005/04/07 15:23
S75	1	("5721923").URPN.	USPAT	OR	ON	2005/07/27 14:10
S76	14	("4319090"   "4497978"   "4912698"   "4959854"   "4974256"   "5161154"   "5189667"   "5263083"   "5303369"   "5325525"   "5329579"   "5408334"   "5473773"   "5628013").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/27 14:11
S45	32	((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3) near2 (echo adj cancel\$5)) with (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)	US-PGPUB; USPAT	OR	ON	2005/07/28 08:50
S77	123	((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3) near2 (echo adj cancel\$5)) with (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)) ((echo adj (canceller cancellation cancelling)) and (processor near2 load))	US-PGPUB; USPAT	OR	ON	2005/07/28 08:51
S79	3	((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3) near2 (echo adj cancel\$5)) and (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)) ((echo adj (canceller cancellation cancelling)) and (processor near2 load))	EPO; JPO; DERWENT	OR	ON	2005/07/28 08:53
S78	0	((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3) near2 (echo adj cancel\$5)) with (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)) ((echo adj (canceller cancellation cancelling)) and (processor near2 load))	EPO; JPO; DERWENT	OR	ON	2005/07/28 08:53
S81	1	((bypass\$3 disabl\$3) near2 (echo adj cancel\$5)) and (processor and (load allocat\$3 resource))	EPO; JPO; DERWENT	OR	ON	2005/07/28 08:56
S80	0	((bypass\$3 disabl\$3) near2 (echo adj cancel\$5)) with (processor and (load allocat\$3 resource))	EPO; JPO; DERWENT	OR	ON	2005/07/28 08:56
S42	4	((bypass\$3 disabl\$3) near2 (echo adj cancel\$5)) with (processor and (load allocat\$3 resource))	US-PGPUB; USPAT	OR	ON	2005/07/28 08:56
S40	19	(718/104 718/105).ccls. and echo	US-PGPUB; USPAT	OR	ON	2005/07/28 09:55
S83	1	"6882711".pn.	US-PGPUB; USPAT	OR	ON	2005/07/28 10:07
S82	19	(echo adj cancel\$6) and ((filter vector coefficient) near3 (size length number)) with (signal near2 (property characteristic spectrum type attribute trait))	US-PGPUB; USPAT	OR	ON	2005/07/28 10:17
S84	114	(echo adj cancel\$6) and ((filter vector coefficient) near3 (size length number)) with (signal near2 (input))	US-PGPUB; USPAT	OR	ON	2005/07/28 10:18
S85	8	("5657349").URPN.	USPAT	OR	ON	2005/07/28 10:43
S86	3	("5146494"   "5245561"   "5500880").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 10:45
S87	27	("5517435").URPN.	USPAT	OR	ON	2005/07/28 10:59
S89	8	("4328585"   "4377793"   "5245561"   "5381354"   "5402520"   "5426704"   "5517435"   "5615233").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 12:12

S88	0	("6633894").URPN.	USPAT	OR	ON	2005/07/28 12:12
S90	193	(370/286 379/406.\$2.718/104-105).ccls. and @pd>"20040514"	US-PGPUB; USPAT	OR	ON	2005/07/28 15:22
S91	1808	broadcom.as.	US-PGPUB; USPAT	OR	ON	2005/07/28 15:23
S94	48	broadcom.as. and resource and echo	US-PGPUB; USPAT	OR	ON	2005/07/28 15:25
S95	26	(leblanc-wilf\$ stevenson-shawn\$.in.	US-PGPUB; USPAT	OR	ON	2005/07/28 15:26
S74	97	(370/286 379/406.\$2.718/104-105).ccls. and ((bypass\$3 disabl\$3 stop\$4 inhibit\$3 hold\$3)) with (dsp microprocessor cpu computer microcomputer controller processor) and (load\$3 allocat\$3 resource overload\$3 shed\$4 unload\$3 free\$3)	US-PGPUB; USPAT	OR	ON	2005/09/06 13:40
L5	316	(370/286 379/406.08 718/104).ccls. and @ad>"19990725" and @ad<"20010223"	US-PGPUB; USPAT	OR	ON	2005/09/06 13:46
L6	64	(370/286 379/406.08 718/104).ccls. and @pd>"20050701"	US-PGPUB; USPAT	OR	ON	2005/09/06 13:49
L10	90	((estimat\$3 near3 complexity)).clm.	US-PGPUB; USPAT	OR	ON	2005/09/06 13:51